

ABSTRACT OF THE DISCLOSURE

An input/output buffer is operative in an analog mode and a digital mode. The buffer includes a pad, a digital signal line which includes a transmission gate connected to the pad, an analog signal line connected to the pad, an analog/digital mode controller which sets an output
5 level of the digital signal line in the analog mode, and a transmission gate controller which controls the transmission gate when a signal voltage of the pad exceeds a reference voltage.